

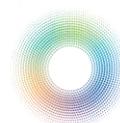
SAMSUNG

Samsung Electronics

2026 Full-Time Opportunity for International Students

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If your imagination knows no bounds,
We want you in our corner.
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To transform technology. The world. And your career.
Join us in the push for progress.
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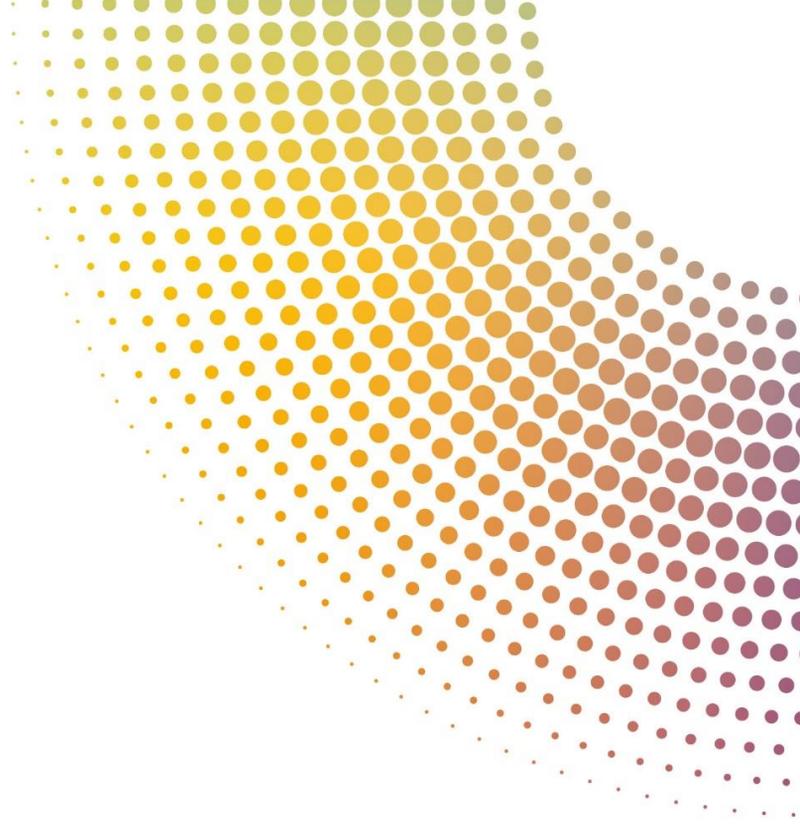
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Samsung Semiconductor

Job Description

Samsung Electronics

Full time Opportunity for International Students

Division	Job	Required Majors	Location
DS	Process Technology	Electrical/Electronics Engineering, Material/Metal Engineering, Chemistry/Chemical Engineering, Mechanical Engineering, Physics	Giheung, Hwaseong, Suwon, Cheonan, Onyang, Pyeongtaek



DS Division

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Job Description

DS Division(Device Solutions)

- **Job : Process Technology**
- **Location : Giheung/Hwaseong/Suwon/Pyeongtaek, Gyeonggi
Cheonan/Onyang, Chungcheong**

R&D on process management/yield improvement/device/measurement technology based on semiconductor knowledge

Role

- **Process Development**
 - Improve process by optimizing mass production and simplifying the overall process
 - Standardize process conditions to improve yield and quality, control bottleneck process
 - Develop eight semiconductor process technologies
(Photo, Etch, Clean, CMP, Diffusion, IMP, Metal, CVD)
 - Quality control by regular process monitoring
 - Resolve persistent defects and improve quality by collaborating with facility technology
- **Manufacturing Science**
 - Improve measurement process productivity, match measurement output
 - Develop product and improve quality by analyzing atomic level device structure and interface reaction
 - Analyze and improve physical and chemical properties of products
 - Increase productivity and maximize efficiency by improving material yield/quality
 - Acquire advanced manufacturing technology (patent, new analysis method, simulation technique, etc.)

Job Description

Requirements

- Electrical Engineering, Material Science, Chemistry and Chemical Engineering, Mechanical Engineering, Physics or related major
- Knowledge of semiconductor basic operation principle, signal processing, data mining, performance analysis

Pluses

- Experience in semiconductor chip design
- Experience with semiconductor development related tool (SPICE Simulation, Schematic Editor, Layout Drawing, Trace32, Power Supply, Logic/Protocol Analyzer, etc.)
- Knowledge of programming language (C++, System Verilog, Python, VBA, etc.)